# Publications relatives à l'APVD et APVDDC

R. TURCHETTA et al. [Collaboration IReS, LEPSI, IPNL, CEA-DAPNIA, IC London, RAL]
*APVD: a CMOS mixed analogue-digital Circuit for the Silicon Tracker in CMS.*
Proceedings of "3rd Workshop on Electronics for LHC Experiments", Imperial College, LONDON, September 1997, CERN/LHCC/97-60, p163-167

R. TURCHETTA et al. [Collaboration IReS, LEPSI, IPNL, CEA-DAPNIA, IC London, RAL]
*Design and Preliminay results of APVD : a fast, low-noise, low-power rad-hard CMOS mixed circuit for the CMS silicon tracker at LHC.*
Proceedings of the SPIE's International Symposium on Optical Science, Engineering and Instrumentation, Conference "EUV, X-Ray and Gamma-Ray Instrumentation for Astronomy IX", San Diego, USA, 19-24 July 1998, Vol. 3445, p329-338

P. SCHMITT et al. [Collaboration IReS, LEPSI, IPNL, CEA-DAPNIA, IC London, RAL]
*Performance of a CMOS mixed analogue-digital circuit (APVD) for the silicon tracker of CMS.*
Proceedings of the "Fourth Workshop on Electronics for LHC Experiment", Rome (Italy), 21-25 September 1998, CERN/LHCC/98-36, p180-184

U. GOERLACH et al. [Collaboration IReS, LEPSI, IPNL]
*Recent Developments and Results on APVD(DMIL) Circuits for Silicon ans MSGC Detectors*
Proceedings of "LEB 99, Fifth LHC Electronics Workshop", Snowmass, Colorado (USA), September 20-24, 1999, p108-112

C. HU-GUO et al. [Collaboration IReS, LEPSI, IPNL, UMM Krakow]
*A 128 Channels Analog Readout Chip (APVD_DC) for DC-Coupled Silicon Detectors of the CMS Tracker*
Proceedings of "LEB 99, Fifth LHC Electronics Workshop", Snowmass, Colorado (USA), September 20-24, 1999, p542-546

C. HU-GUO et al. [Collaboration IReS, LEPSI]
*Low Noise BiCMOS Front-end Amplifier in the DMILL Technology*
Proceedings of "LEB 99, Fifth LHC Electronics Workshop", Snowmass, Colorado (USA), September 20-24, 1999, p537-541

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Proceedings of "6$^{th}$ Workshop on Electronics for LHC Experiments", Krakow (Poland), September 11-15, 2000, p141

C. HU-GUO et al.[Collaboration IReS, LEPSI]
*A Low Noise Low Power CMOS SOI Readout Front-End for Silicon Detectors with Leakage Current Compensation Capability*
IEEE Transactions on Circuits ans Systems-I : Fundamental Theory and Applications, Vol. 48 n°8, August 2001, p1022-1030

C. HU-GUO et al.[Collaboration IReS, LEPSI]
*A Fully Integrated, Low Noise and Low Power BiCMOS Front-end Readout System for Capacitive Detectors*
Analog Integrated Circuits and Signal Processing Vol. 28 (2001) 211-223

U. GOERLACH et al. [Collaboration IReS, LEPSI]
*Test of APV-DMILL circuits with silicon and MSGC micro-strip detectors for CMS*
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