MIMOSA V: real-size prototype

- Real-size prototype: 3.5 cm², 1M pixels
- First ladder concept attempt
- AMS 0.6 μm CMOS with 14 μm epilayer
- Pixel pitch 17x17 μm²
- 4 independent matrices of 512x512 pixels
- Serial analogue readout @ 10 MHz
- Back-thinned down to 120 μm