Development of a Pixellised Ultra-Light Double-Sided Ladder for the ALICE Vertex Detector Upgrade

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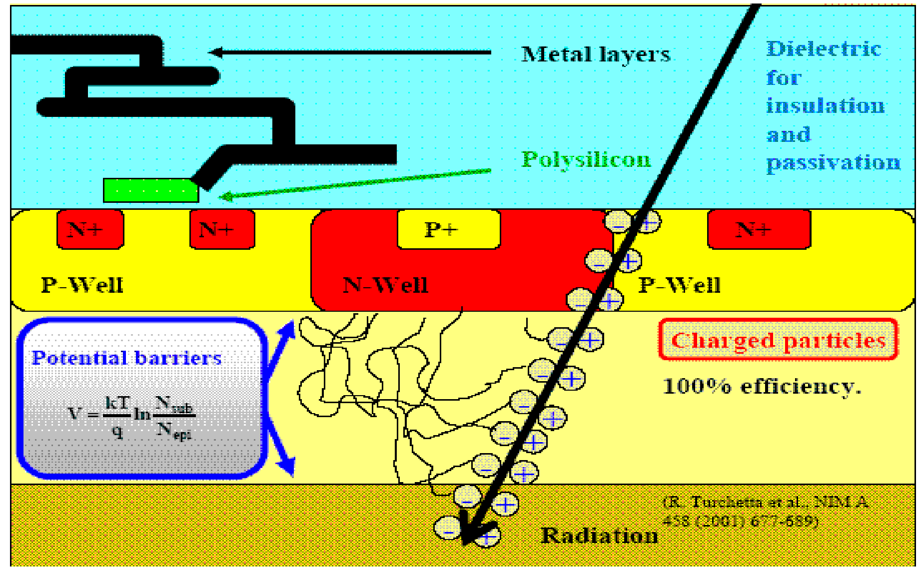
▷ **more information on IPHC Web site:** **http://www.iphc.cnrs.fr/-CMOS-ILC-.html**

Contents

- **Major outcome of 11 years of CMOS pixel sensor R&D at IPHC:**
  ▷ Achieved performances ▷ Applications under way ▷ Plans $\rightarrowtail$ 2012/13
- **Proposal for the ALICE vertex detector upgrade:**
  ▷ Objectives ▷ Deliverables ▷ Context
- **Summary**

Context of R&D: ILC Vertex Detector

- **Sensor requirements defined w.r.t. ILD VTX geometries**



- **P-type low-resistivity Si hosting n-type "charge collectors"**
  - **signal created in epitaxial layer (low doping):**

    Q $\sim$ 80 e-h / $\mu m \mapsto$ signal $\lesssim$ 1000 e$^-$
  - **charge sensing through n-well/p-epi junction**
  - **excess carriers propagate (thermally) to diode with help of reflection on boundaries with p-well and substrate (high doping)**

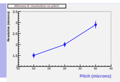
- **Prominent advantages of CMOS sensors:**
  - ◇ **granularity: pixels of $\lesssim$ 10$\times$10 μm^2 $\Rrightarrow$ high spatial resolution**
  - ◇ **low mat. budget: sensitive volume $\sim$ 10 - 15 μm $\Rrightarrow$ total thickness $\lesssim$ 50 μm**
  - ◇ **signal processing μcircuits integrated in the sensors $\Rrightarrow$ compacity, high data throughput, flexibility, etc.**
  - ◇ **other attractive aspects: cost, multi-project run frequency, T$_{room}$ operation, etc.**

▷ ▷ ▷ Attractive balance between granularity, mat. budget, rad. tolerance, r.o. speed & power dissipation

- **Limitations:**
  - ⋈ **Very thin sensitive volume $\rightarrowtail$ impact on signal magnitude (mV !) $\Rrightarrow$ vey low noise FEE**
  - ⋈ **Sensitive volume almost undepleted $\Rrightarrow$ impact on radiation tolerance & speed**
  - ⋈ **Commercial fab. $\Rrightarrow$ fab. param. (doping profile, etc.) not optimal for charged part. detection**
  - ⋈ **etc.**

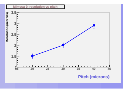
Achieved Performances

with Analog Output Sensors



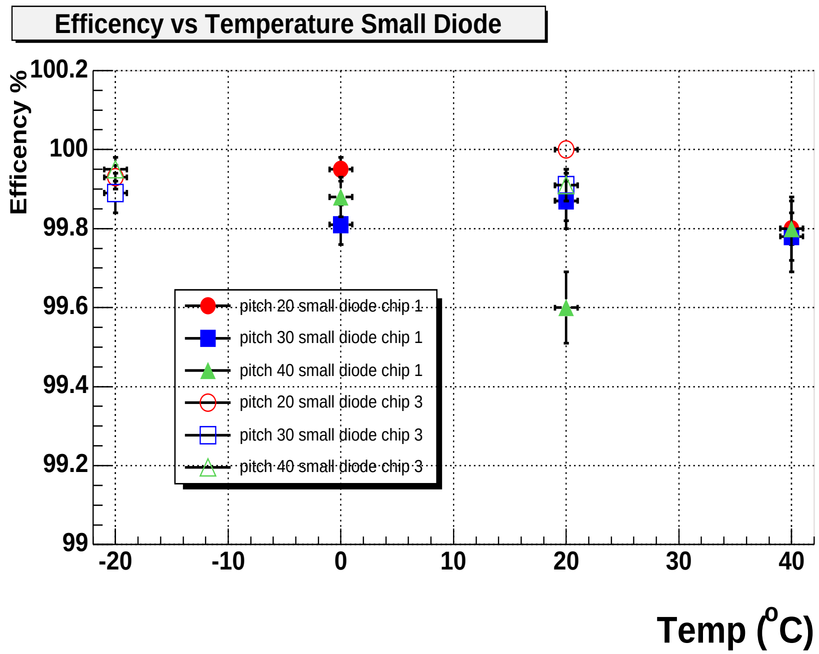
M.I.P. Detection with CMOS Sensors

- $\sim$ 30 different prototypes designed at IPHC (some with IRFU/Saclay)

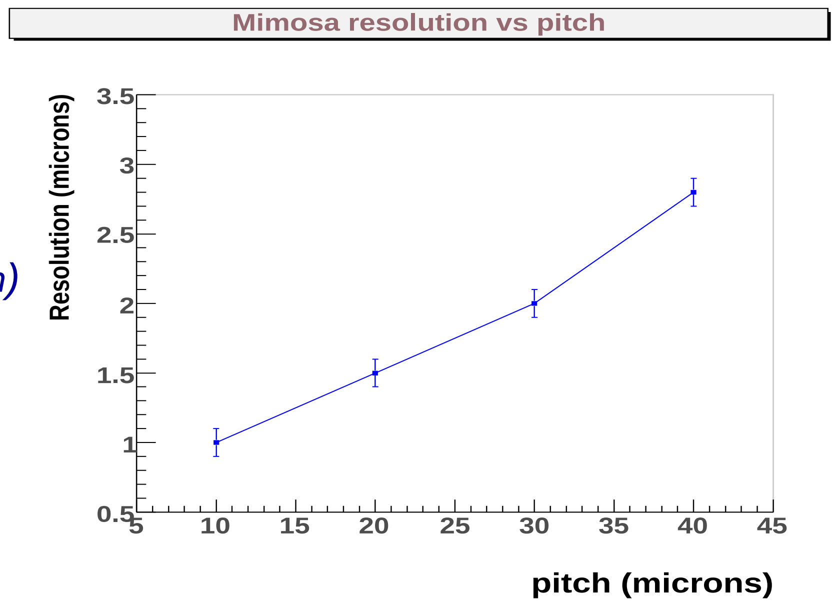


Detection Efficiency & Spatial Resolution

- **Detection efficiency:**
  - ⋇ *Ex: MIMOSA-9 data (20, 30 & 40 μm pitch)*
  - ⋇ $\epsilon_{det} \gtrsim$ *99.5–99.9 % repeatedly observed at room temperature (fake rate $\sim 10^{-5}$)*
  - ⋇ $T_{oper.} \gtrsim 40\ ^{\circ}C$



- **Single point resolution versus pixel pitch:**
  - ⋇ *clusters reconstructed with eta-function, exploiting charge sharing between pixels (12-bit ADC)*
  - ⋇ $\sigma_{\mathbf{sp}} \sim \mathbf{1}\ \mu\mathbf{m}$ *(10 μm pitch)* $\rightarrowtail \lesssim \mathbf{3}\ \mu\mathbf{m}$ *(40 μm pitch)*
  - ⋇ *4-bit ADC simul.* $\Rrightarrow \sigma_{\mathbf{sp}} \lesssim \mathbf{2}\ \mu\mathbf{m}$ *(20 μm pitch)*
  - ⋇ *measured binary output resolution (MIMOSA-16, -22):* $\sigma_{\mathbf{sp}} \gtrsim$ *3.5 & 4.5 μm (18.4 & 25 μm pitch)*



Radiation Tolerance: Condensed Summary

- Ionising radiation tolerance (chips irradiated with 10 keV X-Rays) :
  - ⋇ **Pixels modified against hole accumulations (thick oxide) and leakage current increase (guard ring)**
  - ⋇ **MIMOSA-15 tested with $\sim$ 5 GeV e^- at DESY after 10 kGy exposure : Preliminary results**
    - T = - 20°C, $t_{r.o.} \sim 180\ \mu s$
    - $t_{r.o.} << 1$ms crucial at T_{room}

Integ. Dose	Noise	S/N (MPV)	Detection Efficiency
0	9.0 $\pm$ 1.1	27.8 $\pm$ 0.5	100 %
1 MRad	10.7 $\pm$ 0.9	19.5 $\pm$ 0.2	99.96 $\pm$ 0.04 %

- Non-ionising radiation tolerance (chips irradiated with O(1 MeV) neutrons):
  - ⋇ **MIMOSA-15 (20 μm pitch) tested on DESY e^- beams : Preliminary results**
    - T=-20°C, $t_{r.o.} \sim 700 \mu s$
    - ○ $5.8 \cdot 10^{12} n_{eq}/cm^2$ values with standard & soft cuts

Fluence ($10^{12} n_{eq}/cm^2$)	0	0.47	2.1	5.8 (5/2)	5.8 (4/2)
S/N (MPV)	27.8 $\pm$ 0.5	21.8 $\pm$ 0.5	14.7 $\pm$ 0.3	8.7 $\pm$ 2.	7.5 $\pm$ 2.
Det. Efficiency (%)	100.	99.9 $\pm$ 0.1	99.3 $\pm$ 0.2	77. $\pm$ 2	84. $\pm$ 2.

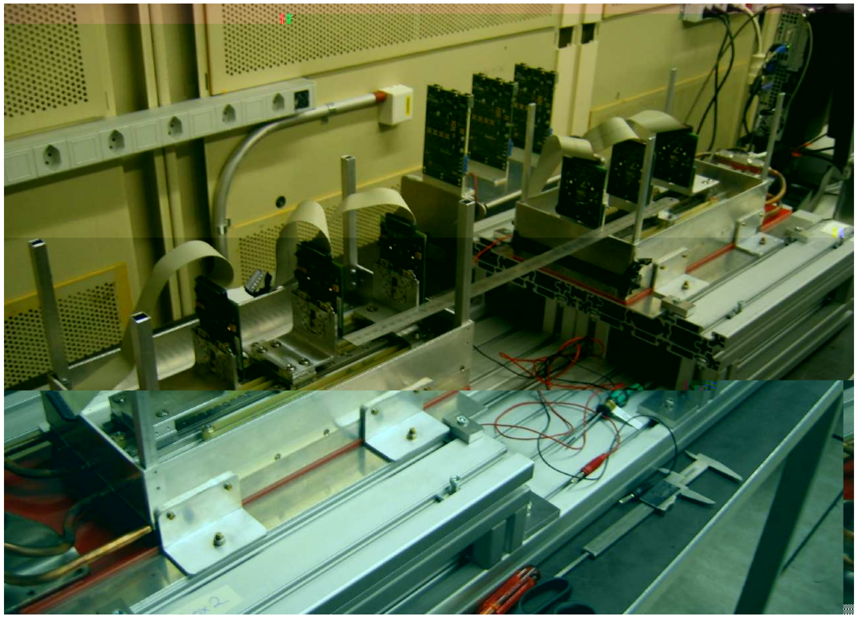
  - ⋇ **MIMOSA-18 (10 μm pitch) tested at CERN-SPS (120 GeV π^- beam) : Preliminary results**
    - T = - 20°C, $t_{r.o.} \sim 3$ ms
    - ○ parasitic 1–2 kGy γ gas $\Rrightarrow$ N $\nearrow$

Fluence (n_{eq}/cm^2)	0	$6 \cdot 10^{12}$	$1 \cdot 10^{13}$
Q_{clust} (e^-)	1026	680	560
S/N (MPV)	28.5 $\pm$ 0.2	20.4 $\pm$ 0.2	14.7 $\pm$ 0.2
Det. Efficiency (%)	99.93 $\pm$ 0.03	99.85 $\pm$ 0.05	99.5 $\pm$ 0.1

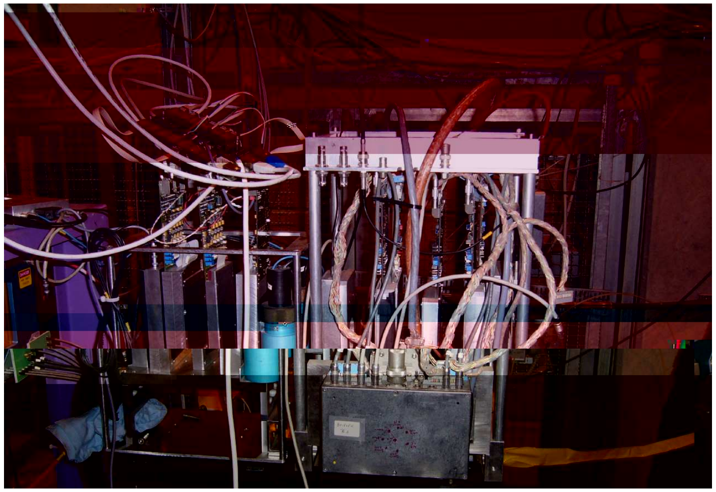
- Conclusion :
  - ⋇ observed ionising radiation tolerance: O(10 kGy)
  - ⋇ observed non-ionising rad. tol.: $> 1 \cdot 10^{13}\ n_{eq}/cm^2$ (10μm pitch) & $2 \cdot 10^{12}\ n_{eq}/cm^2$ (20μm pitch)

Applications of Sensors with Analog Output

- **Beam telescope of the FP6 project EUDET**
  - *2 arms of 3 planes (plus 1 high resolution plane)*
  - $\sigma_{extrapol.} \lesssim 1\ \mu m$ EVEN *with* e^- *(3 GeV, DESY)*
  - *frame read-out frequency* $O(10^2)$ *Hz*
  - *running since '07 CERN-SPS & DESY (numerous users)*
  - *evolution towards* 10^4 *frames/s in 2009, using binary output sensors (see later)*

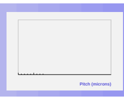


- **Several other applications :**
  - **MIMOSA sensor R&D :** *Pixel telescope of Strasbourg (TAPI)* $\rightarrowtail$
  - **STAR (RHIC) :** *telescope (3 MIMOSA-14) inside apparatus (2007)*
    $\rightarrowtail$ *background measurement, no pick-up !*
  - **CBM (FAIR) :** *MVD demonstrator (double-sided layers) to be used for high precision tracking in HADES (GSI)* $\sim$ *2010*
  - **Spin-offs :** *beta imaging, hybrid photo-detector, dosimetre, ...*



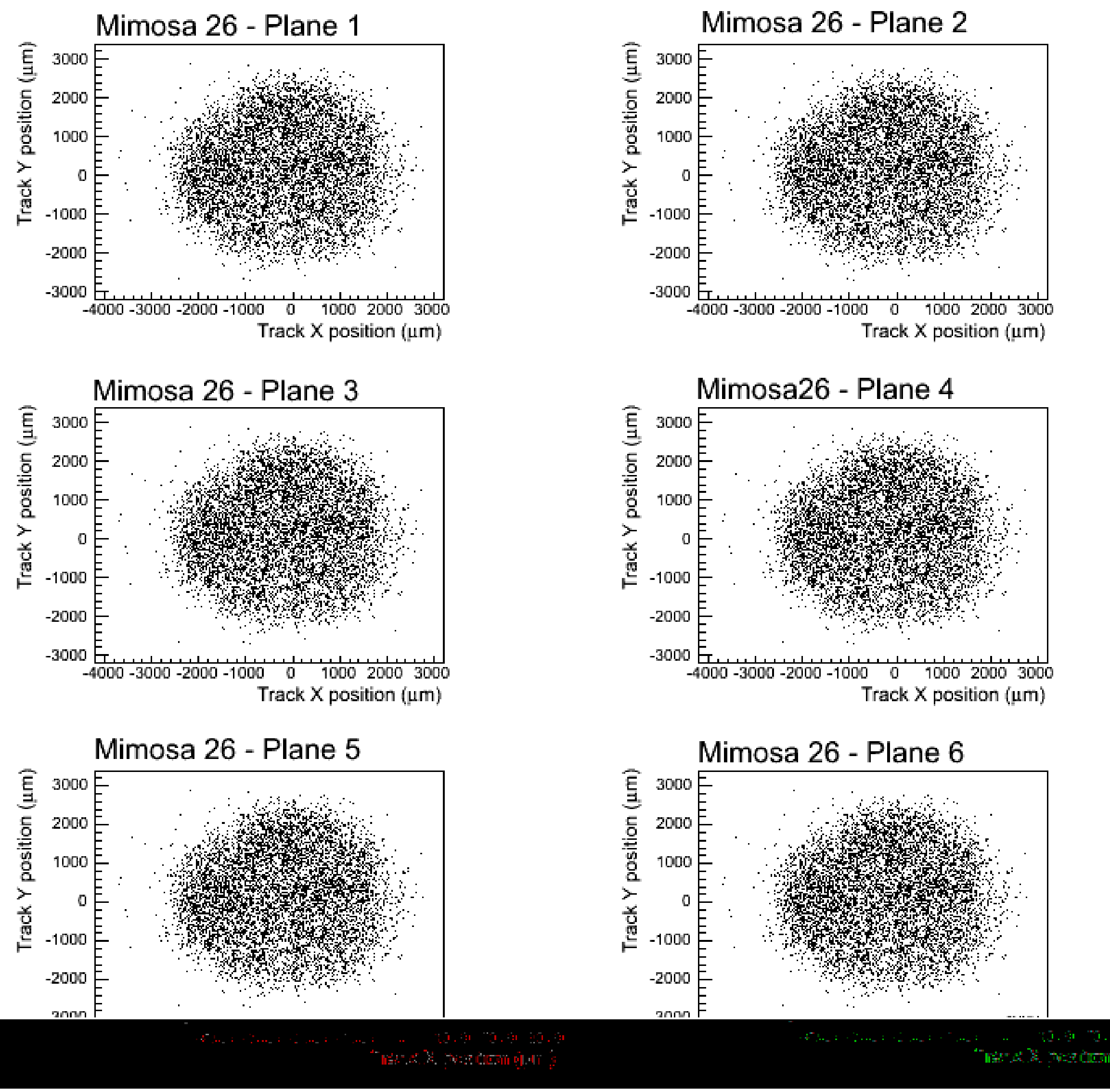
Improving the Read-Out Speed

with Digital Output Sensors


Pitch (microns)

TC Beam Tests: General Remarks

- **6 sensors used:**
  - some thinned to 120 μm
  - assembled in telescope configuration
  - for minimum ionising particle detection performance evaluation

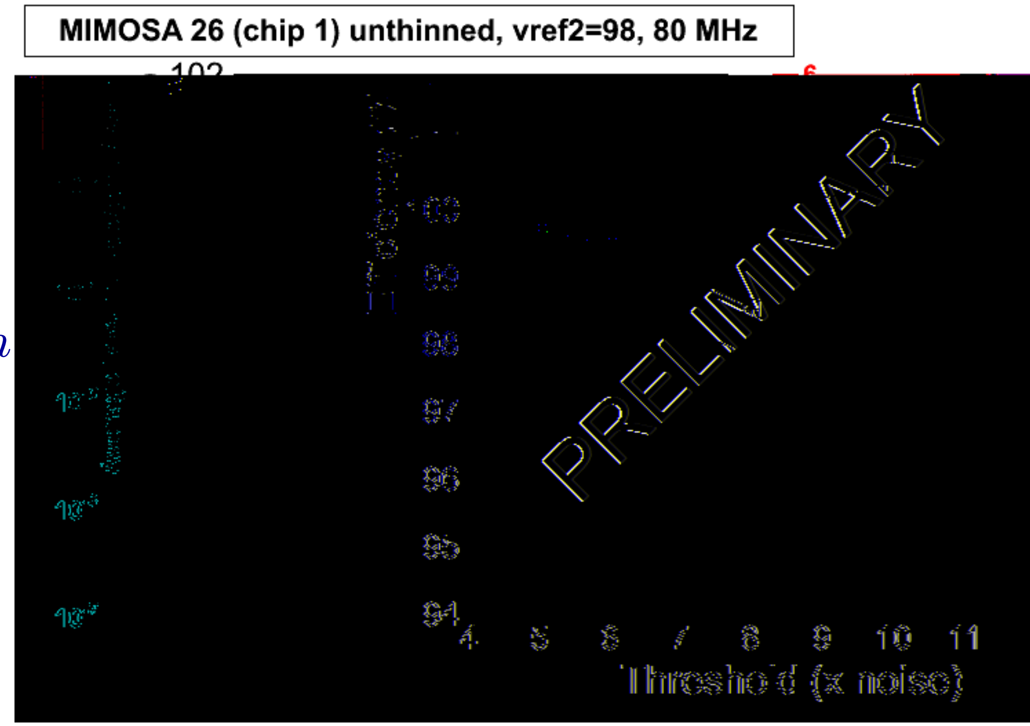


- **CRN-SPS (T4-H6)**
  - $\gtrsim$ 120 GeV π^- beam
  - 10 days of run in Sept. 2009
  - 3×10^6 triggers collected, out of which $\sim$ 80 % events reconstructed

TC Beam Tests: Summary

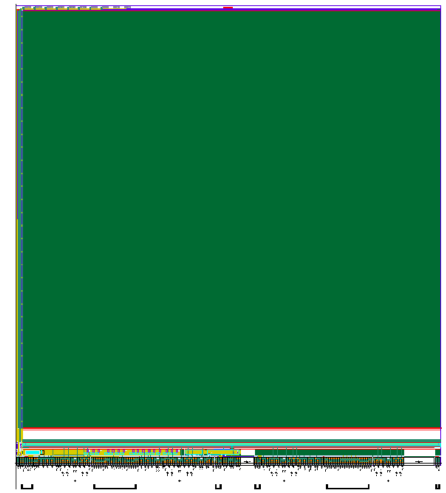
- **TC sensors quite extensively characterised by now (quantitative results still preliminary):**
  - ⋇ TC operating with $\gtrsim$ 99.5 % detection efficiency over the whole sensitive area, with a fake rate $\lesssim$ O(10^{-4})
  - ⋇ Optimal discri. threshold $\sim$ 5–6 $\times$ Noise value
  - ⋇ $\sigma_{sp}^{TC} \lesssim$ 4.5 $\pm$ 0.2 μm (preliminary)

    $\hookrightarrow$ room for improvement towards $\sigma_{sp}^{TC} \lesssim$ 4 μm

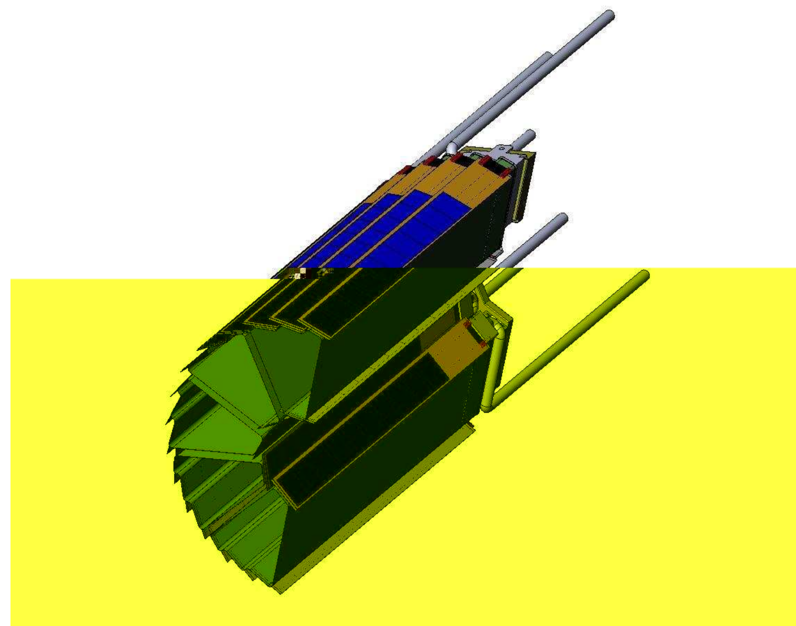


- **Conclusion:**
  - ⋇ TC operationnal for the final EUDET BT ✓
  - ⋇ TC architecture validated for devt of application specific sensors:
    - ▷ STAR-PIXEL, CBM-MVD, ILD-VTX, ...

- **1st generation sensor for the HFT-PIXEL of STAR (PHASE-1):**
  - **full scale extension of MIMOSA-22 (no $\emptyset$)**
  - *640x640 pixels (30 μm pitch) $\Rrightarrow$ active surface: 19.2 x 19.2 mm^2*
  - *integration time : 640 μs*
  - *designed and fabricated in 2008 $\rightarrowtail$ currently under production test at LBNL*
  - *3 + 9 ladders to equip with 10 sensors thinned to 50 μm (1/4 of PIXEL)*
  - *1st physics data expected in 2011*



- **Final HFT-PIXEL sensor :**
  - **MIMOSA-26 with active area $\times$ 1.8**
    & improved rad. tolerance
    $\hookrightarrow$ *1088 col. of 10^3 pixels (20 x 18.5 mm^2)*
  - **Pitch : 18.4 μm $\rightarrowtail$ $\sim$ 1.1 million pixels**
  - **Integration time $\lesssim$ 200 μs**
  - *Design in 2009 $\rightarrowtail$ fab. Q1 (2010)*
    $\rightarrowtail$ *1st physics data expected in 2012/13*



From TC to Faster Devices

- **Move from 0.35 μm to feature size $\leq$ 0.18 μm**

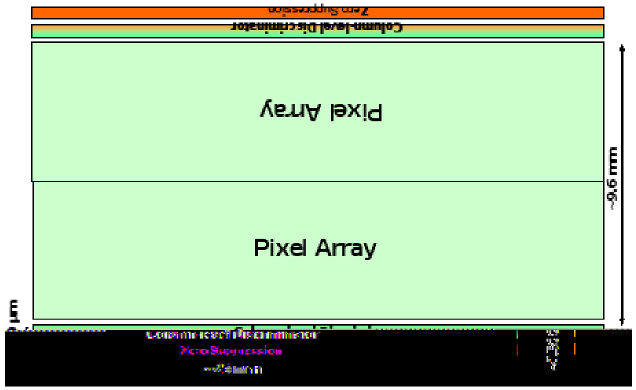
  $\Rrightarrow$ improved clock frequency, more metal layers, more compact peripheral circuitry, etc.

- **ILD VTX inner layers ($t_{int} \sim$ 25 - 50 μs) :**

  ⋇ double-sided r.o. $\Rrightarrow$ twice shorter (= faster) columns

  ⋇ pitch $\lesssim$ 15 μm

  ⋇ binary r.o. $\Rrightarrow$ $\sigma_{sp} \lesssim 3\ \mu m$



- **Implement elongated pixels on one side of double-ladder:**

  ⋇ pixel pitch of $\sim$ 14$\times$50–80 μm

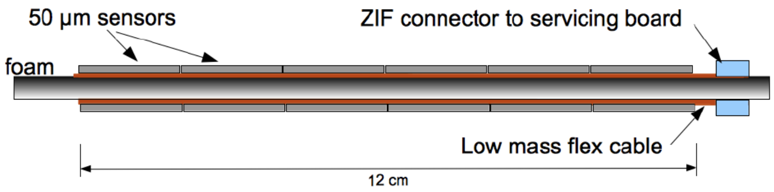
  ⋇ reduced nb of pixels per column

  $\Rrightarrow$ shorter read-out (goal: factor 4–6)

  ⋇ depleted epitaxial layer

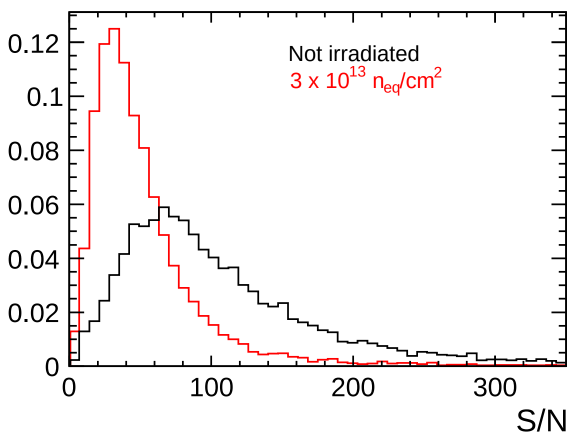
  $\Rrightarrow$ wider sensing diode spacing (next slide)

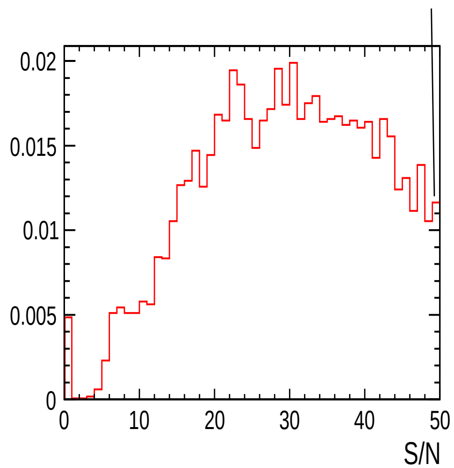
  $\hookrightarrow$ $\lesssim$ 10 μs may be reachable



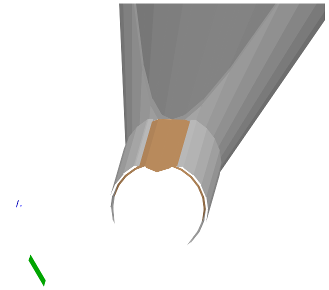
High Resistivity Sensitive Volume

- **Advantages of depleted epitaxial layer:**
  - *faster charge collection ($<$ 10 ns) $\Rrightarrow$ faster frame read-out frequency possible*
  - *shorter minority charge carrier path length $\Rrightarrow$ improved tolerance to non-ionising radiation*
- **Exploration of 0.6 μm techno:** $\sim$ 15 μm thick epitaxy ; V$_{dd}$ $\leq$ 5 V ; $\rho \sim O(10^3)\Omega \cdot cm$
  - MIMOSA-25: fabricated in 2008 & tested at CERN-SPS before/after O(1 MeV) neutron irradiation





- **PLUME project**
  - **Pixelised Ladder using Ultra-light Material Embedding**



- **Objectives :**
  - achieve a double-sided ladder prototype

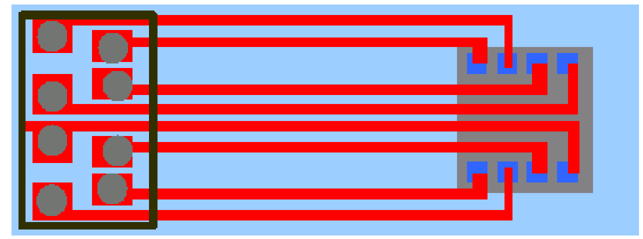
    for an ILC vertex detector by 2012
  - evaluate benefits of 2-sided concept (mini-vectors) :

    σ_{sp}, alignment, shallow angle pointing, elongated vs square pixels

- **HP-2 project ▷ WP-26 (ULISI)** ≡ **SE**nsor **R**ow **W**rapped **I**n **E**xtra-**T**hin film (SERWIET)

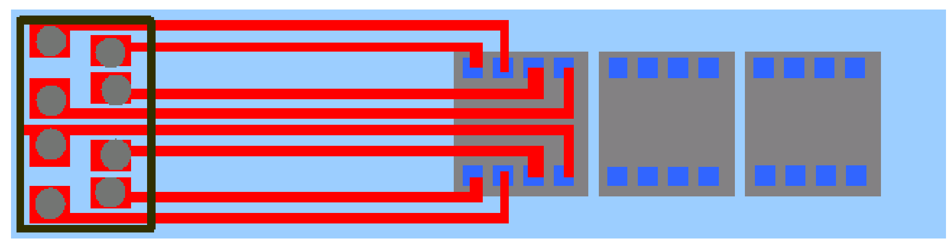
- **Objectives :**
  - ⋇ achieve a sensor assembly mounted on flex and wrapped in polymerised film with < 0.1 % X_0 in total
  - ⋇ evaluate possibility of mounting supportless ladder on cylindrical surface (serving as mechanical support)
  - ⋇ proof of principle expected in 2012

Proto 1 ▷ Spring 2010



- **Working programme:**
  - ⋇ prototype Nr. 1 (2010) made of 1 sensor : MIMOSA-18 (analog output, $\lesssim$ 4 ms)
  - ⋇ prototype Nr. 2 (2011) made of 3 sensors : TC (digital output, $\lesssim 110\ \mu s$)

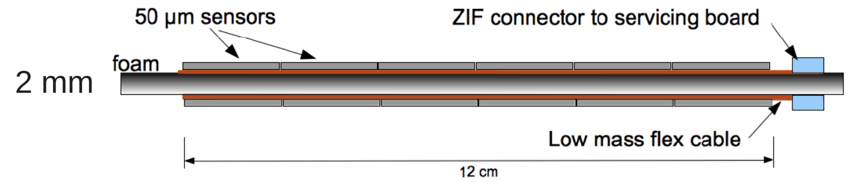
Proto 2 ▷ Summer 2011



- **Context of development:**
  - ⋇ Collaboration with IKF-Frankfurt and GSI/Darmstadt (CBM coll.)
  - ⋇ **Synergy with Vertex Detector R&D for CBM, ILC, etc.**

Improve capability of displaced vertex reconstruction for charm tagging :

- Equip free space left by vacuum tube replacement with
  1 or 2 double-sided high precision pixellised ladders
  (and envisage equipping beam pipe ???)



- Ambitionned performances:
  - ⋇ thin pixel sensors (50 μm) offering a single point resolution $< 5\ \mu m$
  - ⋇ ladder with complete material budget $\sim$ 0.2-0.3 % X_0
  - ⋇ 2 contiguous impacts per particle detected $\Rrightarrow$ several benefits for detection performances:
    - ◇ resolution (impact param.) ◇ track reconstruction ◇ alignment (crucial !)

Context : R&D on-going at IPHC-Strasbourg

- Devt of thin, high resolution, CMOS pixel sensors for STAR & CBM (since 2000 & 2003)
  $\Rrightarrow$ design of sensor architecture $\sim$ mature (sensor operational on EUDET/FP-6 telescope)
- Devt of ultra-light ladders for CBM, ILC, etc. (since 2008) $\rightarrowtail$ WP-26 (HP-2)
  $\Rrightarrow$ several approaches investigated $\rightarrowtail$ prototyping finalised $\sim$ 2012/13
- First data collected with STAR-PIXEL (open charm) in 2012/13
  $\Rrightarrow$ single-sided ladders: $\sigma_{sp} \lesssim 4\ \mu m$, material budget $\sim$ 0.3 % X_0

CMOS pixel sensors :

- Ambitionned performances (room T):

  ⋇ $\sigma < 5\ \mu m$ ✓ ⋇ thickness $\lesssim$ 50 μm ✓ ⋇ $t_{ro} \sim$ 10-20 μs ⋇ 1 MRad & $10^{14} n_{eq}/cm^2$

  $\Rightarrow$ on-going R&D on t_{ro} ($< 50 \mu s$) & rad. tolerance ($>$ 300 kRad & $>$ $10^{13} n_{eq}/cm^2$)

- 3 sensor generations under development:

  ⋇ 2D with undepleted sensitive volume $\rightarrowtail$ $t_{ro} \sim$ 10-20 μs & 1 MRad (2011/12)

  ⋇ 2D with depleted sensitive volume (elongated pixels) $\rightarrowtail$ 1 MRad & $10^{14} n_{eq}/cm^2$ (2013)

  ⋇ 3D with depleted sensitive volume $\rightarrowtail$ $t_{ro} \lesssim$ 5 μs (2015)

Ladder developed along 2 parallel approaches:

- Classical : sensors $\oplus$ flex cable $\oplus$ SiC foam $\oplus$ flex cable $\oplus$ sensors

  $\Rightarrow$ $\lesssim$ 0.3 % X_0 for the whole 2-sided ladder

- Innovating : sensors (35 μm) wrapped in polymerised film ($<$ 10 μm)

  $\Rightarrow$ $\lesssim$ 0.1 % X_0 for 1 unsupported layer (sensors $\oplus$ flex cable $\oplus$ film)

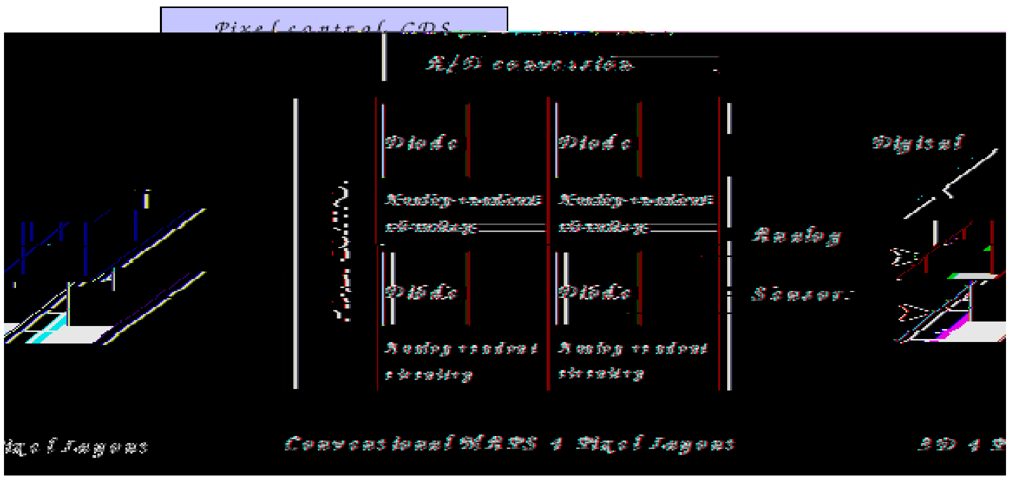
  $\hookrightarrow$ adaptable to various mechanical supports $\rightarrowtail$ beam pipe ?

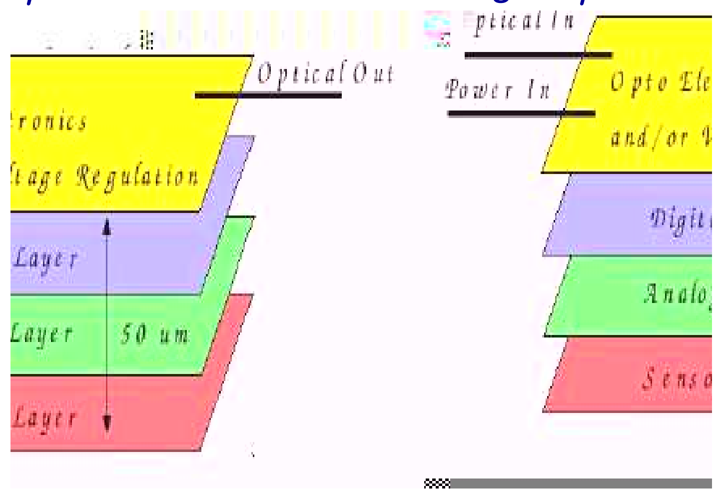
Using 3DIT to Improve CMOS Sensor Performances

- **3D Integration Techno.** allow integrating high density signal processing μcircuits inside small pixels by stacking ($\sim$ 10 μm) thin tiers interconnected at pixel level

- **3DIT are expected to be particularly beneficial for CMOS sensors :**
  - ⋇ combine different fab. processes $\Rrightarrow$ chose best one for each tier/functionnality
  - ⋇ alleviate constraints on peripheral circuitry and on transistor type inside pixel, etc.

- **Split signal collection and processing functionnalities :**
  - ⋇ *Tier-1: charge sensing* ⋇ *Tier-2: analog-mixed μcircuits* ⋇ *Tier-3: digital μcircuits*





- **First chips fabricated in 2009 within 3DIC Consortium (2-Tier 130 nm technology):**
  - ⋇ *1 chip with sensitive area subdivided in "small" matrices running* INDIVIDUALLY *in fast rolling shutter mode $\Rrightarrow$ few μs r.o. time may be reached (?)*
  - ⋇ *1 chip aiming to combine 2-Tier read-out chip with sensing chip featuring* DEPLETED *epitaxial layer*

- **Proposal: investigate possibility of translating ultra-light pixellised ladders developed for CBM & ILC to the specifications of the ALICE vertex detector upgrade, relying on 2 basic components:**
  - ⁜ thin, depleted, CMOS pixel sensors (baseline 2D, potentially 3D)
  - ⁜ ultra-light double-sided ladder complemented with potential pixelised beam pipe
- **CMOS pixel sensors developed for EUDET, STAR, CBM, ILC:**
  - ⁜ offer necessary single point resolution and material budget for very high precision vertexing
  - ⁜ have reached required maturity to be used in large (heavy ion) experiments (e.g. STAR-HFT)
    - $\Rightarrow$ may be relevant for ALICE vertex detector upgrade $>$ 2015: barrel & forward (CBM-MVD)
- **Ultra-light ladders developed for CBM & ILC:**
  - ⁜ double-sided ladder with $\lesssim$ 0.3 % X_0, expected to provide excellent pointing accuracy: impact parameter resolution even at shallow angle, track link with faster detectors, ...
  - ⁜ supportless ladder expected to offer $<$ 0.1 % X_0 $\rightarrowtail$ explore possibility to equip beam pipe ?
- **Synergetic context to exploit:**
  - ⁜ CMOS sensor development with IRFU/Saclay
  - ⁜ ladder development within HP-2 (Frankfurt, GSI), PLUME collaboration (ILC teams)
  - ⁜ perspective: AIDA FP-7 proposal (study of high precision vertex detector alignment issues)